ModSTACK™ HD

6MS20017E43W37032



Preliminary data

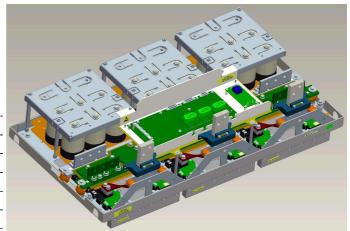
General information

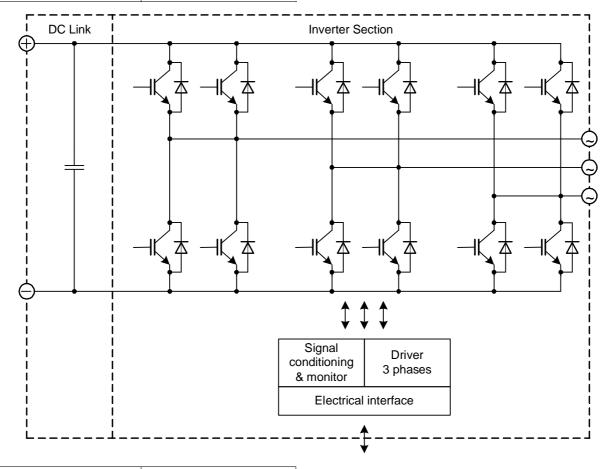
IGBT Stack for typical voltages of up to 690 V_{RMS} Rated output current 1200 A_{RMS}

- High power converterWind powerMotor drives

- · PrimePACKTM3 module with integrated NTC
- · Extended operational temperature · Low V_{cesat}

Topology	B6I
Application	Inverter
Load type	Resistive, inductive
Semiconductor (Inverter Section)	6x FF1000R17IE4
DC Link	7.2 mF
Heatsink	Water cooled
Implemented sensors	Current, voltage, temperature
Driver signals IGBT	Electrical
Sales - name	6MS20017E43W37032
SP - No.	SP000957318





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Absolute maximum rated values

Collector-emitter voltage	IGBT; T _{vj} = 25°C	V _{CES}	1700	V
Repetitive peak reverse voltage	Diode; T _{vj} = 25°C	V_{RRM}	1700	V
DC link voltage		V _{DC}	1250	V
Insulation management	according to installation height of 2000 m	V _{line}	690	V _{RMS}
Insulation test voltage	according to EN 50178, f = 50 Hz, t = 1 s	V _{ISOL}	2.5	kV _{RMS}
Repetitive peak collector current inverter section (IGBT)	$t_p = 1 \text{ ms}$	I _{CRM2}	2500	А
Repetitive peak forward current inverter section (Diode)	t _p = 1 ms	I _{FRM2}	2500	А
I²t-value inverter section (Diode)	$V_R = 0 \text{ V}, t_p = 10 \text{ ms}, T_{vj} = 125 ^{\circ}\text{C}$	l²t	252	kA²s
Continuous current inverter section		I _{AC2}	1200	A _{RMS}
Junction temperature	under switching conditions	T _{vjop}	150	°C
Switching frequency inverter section		f _{sw2}	4	kHz

Notes

Further maximum ratings are specified in the following dedicated sections

Characteristic values

DC Link			min.	typ.	max.	
Rated voltage		V _{DC}		1100	1200	V
Over voltage shutdown				1250		V
Capacitor	1 s, 18 p, rated tol. ±10 %	C _{DC}		7.2		mF
		type		Foil		
Maximum ripple current	per device	Iripple			49	ARMS
Balance or discharge resistor	per DC link unit	R _b		15.7		kΩ

Notes
Operation above 1100 V subject to reduced operating time according to EN 61071

Inverter Section			min.	typ.	max.	
Rated continuous current	$ \begin{array}{l} V_{DC} = 1100 \text{ V}, V_{AC} = 690 V_{RMS}, \cos(\phi) = 0.85, \\ f_{AC \text{ sine}} = 50 \text{ Hz}, f_{sw} = 3000 \text{ Hz}, T_{inlet} = 40 ^{\circ}\text{C}, \\ T_{j} \leq 150 ^{\circ}\text{C} \end{array} $	lac			1200	Arms
Rated continuous current for 150% overload capability	I _{AC 150%} = 1290 A _{RMS} , t _{on over} = 60 s, T _j ≤ 150 °C	IAC over1			860	Arms
Rated continuous current for 150% overload capability	$I_{AC\ 150\%}$ = 1410 A _{RMS} , $t_{on\ over}$ = 3 s, T_{j} ≤ 150 °C	I _{AC over2}			940	A _{RMS}
Over current shutdown	within 15 μs	lac oc		2500		A _{peak}
Power losses	$\begin{array}{l} I_{AC} = 1200 \text{ A, } V_{DC} = 1100 \text{ V, } V_{AC} = 690 \text{ V}_{RMS}, \\ \cos(\phi) = 0.85, f_{AC \text{ sine}} = 50 \text{ Hz, } f_{sw} = 3000 \text{ Hz,} \\ T_{inlet} = 40 \text{ ^{\circ}C, } T_{j} \leq 150 \text{ ^{\circ}C} \end{array}$	P _{loss}		19500		W

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Controller interface

Driver and interface board	ref. to separate Application Note			DR110		
			min.	typ.	max.	
Auxiliary voltage		Vaux	18	24	30	V
Auxiliary power requirement	V _{aux} = 24 V	Paux		40		W
Digital input level	resistor to GND 1.8 kΩ, capacitor to GND 4 nF,	V _{in low}	0		4	V
0	logic high = on, min. 15 mA	V _{in high}	11		15	V
Digital output level	open collector, logic low = no fault, max. 15 mA	V _{out low}	0		1.5	V
		V _{out high}		15		V
Analog current sensor output inverter section	load max 1 mA, @ 1200 A _{RMS}	VIU ana2 VIV ana2 VIW ana2	3.7	3.8	3.9	V
Analog DC link voltage sensor output	load max 1 mA, @ 1100 V	V _{DC} ana	7.7	7.9	8.1	V
Analog temperature sensor output inverter section (NTC)	load max 1 mA, @T _{NTC} = 71 °C, corresponds to T _j = 148 °C at rated conditions	V _{Theta NTC2}		8.3		V
Analog temperature sensor output inverter section (Simulated)	load max 1 mA, @T _{NTC} = 71 °C, corresponds to T _j = 148 °C at rated conditions	V _{Theta sim2}		8.7		V
Over temperature shutdown inverter section		V _{Error OT2}		9.1		V

System data				min.	typ.	max.	
EMC robustness	MC robustness according to IEC 61800-3 at named interfaces	power	V_{Burst}		2		kV
		control	V _{Burst}		1		kV
		aux (24V)	V _{surge}		1		kV
Storage temperature			T _{stor}	-40		80	°C
Operational ambient temperature	PCB, DC link capacitor, bus bar, excluding medium	g cooling	T _{op amb}	-25		55	°C
Cooling air velocity	PCB, DC link capacitor, bus bar, standard atmosphere		Vair	2			m/s
Humidity	no condensation		Rel. F	0		95	%
Vibration	according to IEC 60721					5	m/s²
Shock	according to IEC 60721					40	m/s²
Protection degree					IP00		
Pollution degree					2		
Dimensions	width x depth x height			1090	596	342	mm
Weight					153		kg

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Heatsink water cooled			min.	typ.	max.	
Water flow	according to coolant specification from Infineon	ΔV/Δt	45			dm³/min
Water pressure					8	bar
Water pressure drop	at 45 dm³/min water flow	Δp		200		mbar
Coolant inlet temperature		T _{inlet}	-40		55	°C
Thermal resistance heatsink to ambient	per switch	R _{th,ha}		0.038		K/W
Cooling channel material				Copper		

Composition of coolant: Water and 52 vol. % Antifrogen N

Overview of optional components	Unit 1	Inverter Section	Unit 3
Parallel interface board			
Optical interface board			
Voltage sensor		×	
Current sensor		×	
Temperature sensor		×	
Temperature simulation		×	
DC link capacitors		×	
Collector-emitter Active Clamping		×	

Notes
Setting of Active Clamping TVS-Diodes: V_Z = 1280 V

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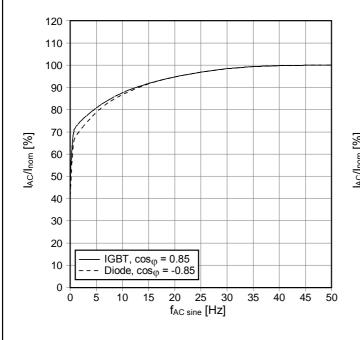
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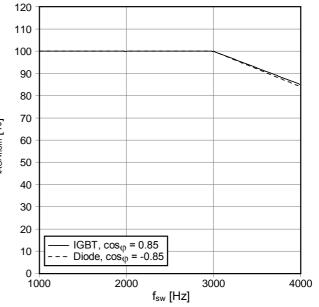


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 $f_{AC\;sine}$ - derating curve IGBT (motor), Diode (generator) V_{DC} = 1100 V, V_{AC} = 690 $V_{RMS},\,f_{sw}$ = 3 kHz, cos_{ϕ} = ± 0.85 T_{inlet} = 40°C and nom. cooling conditions

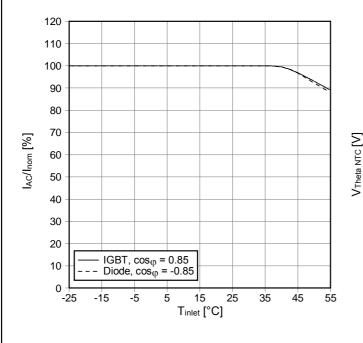
 f_{sw} - derating curve IGBT (motor), Diode (generator) V_{DC} = 1100 V, V_{AC} = 690 $V_{\text{RMS}},$ $f_{\text{AC sine}}$ = 50 Hz, \cos_ϕ = ± 0.85 T_{inlet} = 40°C and nom. cooling conditions

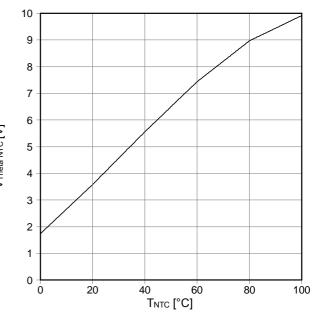




 T_{inlet} - derating curve IGBT (motor), Diode (generator) V_{DC} = 1100 V, V_{AC} = 690 $V_{RMS},\,f_{AC\,\,\text{sine}}$ = 3 kHz, $f_{AC\,\,\text{sine}}$ = 50 Hz cos_{ϕ} = ±0.85 and nom. cooling conditions

Analog temperature sensor output $V_{\text{Theta NTC}}$ Sensing NTC of IGBT module





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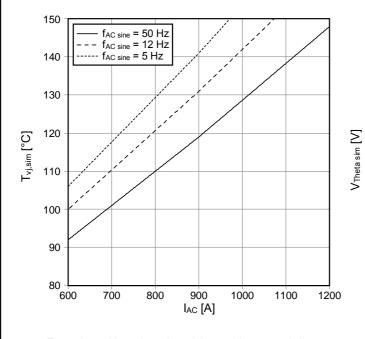
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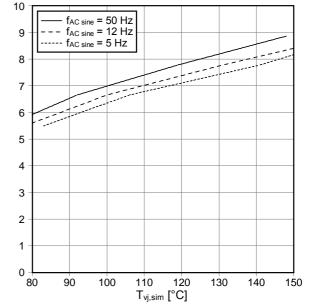


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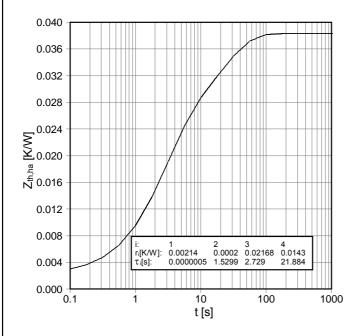
 $T_{vj,sim}$ vs. I_{AC} - Simulated junction temperature V_{DC} = 1100 V, V_{AC} = 690 $V_{RMS},\,f_{sw}$ = 3 kHz T_{inlet} = 40°C and nom. cooling conditions

Analog temperature sensor output $V_{\text{Theta sim}}$ V_{DC} = 1100 V, V_{AC} = 690 V_{RMS} , f_{sw} = 3 kHz, T_{inlet} = 40°C and nom. cooling conditions





 $Z_{\text{th,ha}} \text{ - thermal impedance heatsink to ambient per switch} \\ \text{nom. cooling conditions}$



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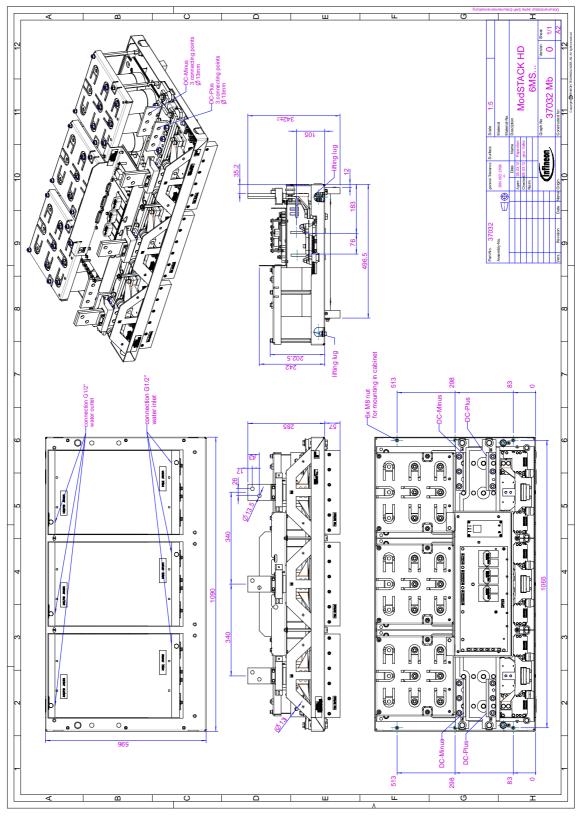
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Mechanical drawing



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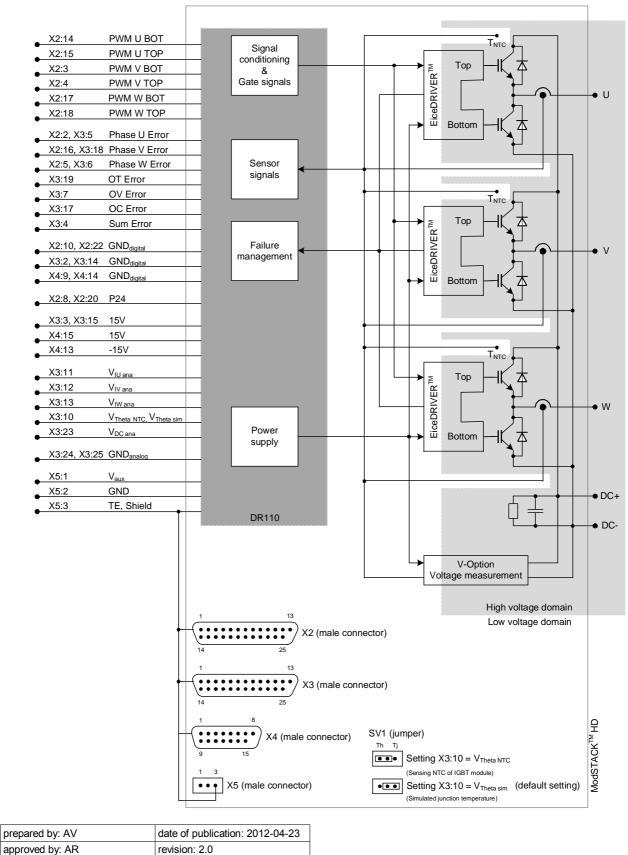
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Circuit diagram



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- to perform joint Risk and Quality Assessments;
- the conclusion of Quality Agreements;
- to establish joint measures of an ongoing product survey, and that we may make delivery depended on the realization of any such measures.

If and to the extent necessary, please forward equivalent notices to your customers.

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Safety Instructions

Prior to installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced. To installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced.

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